

Application No.: 10/037,896

Docket No.: JCLA7228

**In The Claims:**

Claim 1. (currently amended) A power controller for a computer system having a microprocessor therein, wherein the power controller receives a voltage identification signal transmitted from the microprocessor, the power controller comprising:

a first voltage identification digital/analogue converter for receiving the voltage identification signal and outputting a first voltage specification signal;

a second voltage identification digital/analogue converter for receiving the voltage identification signal and outputting a second voltage specification signal; and

a selector coupled to the first identification digital/analogue converter and the second identification digital/analogue converter for outputting the voltage specification signal from the first voltage identification digital/analogue converter or the second voltage identification digital/analogue converter based on a microprocessor selection signal generated by the computer signal.

Claim 2. (original) The power controller of claim 1, wherein the selector is a multiplexer that couples with the first voltage identification digital/analogue converter and the second voltage identification digital/analogue converter for receiving the microprocessor selection signal and outputting the first voltage specification signal or the second voltage specification signal.

**Claims 3-5. (cancelled)**

Claim 6. (original) The power controller of claim 1, wherein the power controller also produces a terminal voltage according to a microprocessor selection signal.

**Claims 7-9. (cancelled )**

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Claim 10. (original) A power controller for a computer system having a microprocessor selected from a panel of processors, wherein the computer system outputs a microprocessor selection signal and a microprocessor type signal according to the particular microprocessor, the power controller comprising:

a plurality of voltage identification digital/analogue converters for receiving a voltage identification signal from the particular microprocessor and outputting a plurality of voltage identification signals; and

a selector coupled to the voltage identification digital/analogue converters and outputting one of the voltage identification signals according to the microprocessor selection signal and the microprocessor type signal, wherein each voltage identification digital/analogue converters corresponds with one type of the microprocessor for producing a voltage specification signal that suits the particular processor.

Claim 11. (original) The power controller of claim 10, wherein the selector includes a multiplexer coupled to the voltage identification digital/analogue converters for outputting one of the voltage specification signals.

**Claims 12-13. (cancelled )**

14. (original) The power controller of claim 10, wherein the power controller also provides a terminal voltage for the particular microprocessor according to the microprocessor selection signal and the microprocessor type signal.

Claim 15. (original) A computer system capable of supporting a multiple of processor types, comprising:

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a first microprocessor socket for plugging a microprocessor; and

a power controller coupled to the first microprocessor socket for receiving a voltage identification signal from the microprocessor and a microprocessor selection signal from the computer system so that the computer system can provide a core voltage to the microprocessor, wherein the power controller determines type of microprocessor plugged in the socket according to the microprocessor selection signal, if the microprocessor belongs to a first type of microprocessor, the power controller outputs a first voltage specification signal as well as a first terminal voltage, and if the microprocessor belongs to a second type of microprocessor, the power controller outputs a second voltage specification signal as well as a second terminal voltage.

Claim 16. (original) The computer system of claim 15, wherein the power controller further includes:

a first voltage identification digital/analogue converter for receiving the voltage identification signal and outputting a first voltage specification signal that meets the requirement of the first type microprocessor; and

a second voltage identification digital/analogue converter for receiving the voltage identification signal and outputting a second voltage specification signal that meets the requirement of the second type microprocessor.

Claim 17. (original) The computer system of claim 16, wherein the power controller further includes a multiplexer that couples with the first voltage identification digital/analogue

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converter and the second voltage identification digital/analogue converter and outputs the first voltage specification signal or the second voltage specification signal.

**Claims 18-19. (cancelled )**

Claim 20. (original) The computer system of claim 15, wherein the system further includes a second microprocessor socket with the microprocessor plugged either into the first microprocessor socket or the second microprocessor socket, the power controller receives a microprocessor type signal from the computer system, the power controller further comprising:

a third voltage identification digital/analogue converter for receiving the voltage identification signal and outputting a third voltage specification signal that meets requirements for a third type microprocessor; and

a fourth voltage identification digital/analogue converter for receiving the voltage identification signal and outputting a fourth voltage specification signal that meets requirements for a fourth type microprocessor, wherein the power controller determines type of microprocessor according to the microprocessor selection signal and the microprocessor type signal, if the microprocessor belongs to a third type processor, the power controller outputs the third voltage specification signal as well as a third terminal voltage, and if the microprocessor belongs to a fourth type processor, the power controller outputs the fourth voltage specification signal as well as a fourth terminal voltage.